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RULAN T. MANGUE 202,408,4233

September 13, 2007

VIA FACSIMILE ONLY

Examiner Hong Kim United States Patent and Trademark Office phone: 571-272-4181 fax: 571-273-4181

Application No. 19/788,725-Support for amendments

Dear Examiner Hong Kim:

During our telephone conference today, you asked me to send a fax to you outlining the support for the amendments to the specification and drawings proposed by Applicant in the Amendment after Allowance filed July 31, 2007.

For your reference, I have included a marked-up version of paragraph [0018] below, showing the proposed amendments:

[0018] FIG. 3 is a diagram that illustrates reading from the semi-trace cache and FIG. 4 is a flow diagram 400 that shows functional operation of semi-trace cache 20. As shown in FIG. 4, block 402 shows that an instruction is fetched from the current line of semi-trace cache 20. At block 430, a determination is made whether the instruction is retirement there were the most new a superimisary to make wheme the instructional as started from Cache and as a simp, branch, call, or return instruction. The conditions of block 450 are met, the Cache portion is indexed with the address of the current induction at block 431. The That is technel instruction at block 431. The That is indicated by block 432. At Block 432, the summit the colon is inserted into semi-tire, indicated by block 404. At Block 432, the summit the colon is inserted into semi-tire, indicated by block 404. At Block 432, the summit the colon is inserted into semi-tire, indicated by block 404. At Block 432, the summit the colon is inserted into semi-tire, indicated by block 404. At Block 432, the summit the colon is made in the semi-time of the colon in the co to whether the executed instruction causes processor 12 to take a change in the flow-ofcontrol. Different actions may be taken depending on whether processor 12 is running from the TCache portion or the ICache portion. For instance, when running from the TCache portion a change in flow-of-control may occur when a branch is mispredicted or an end of the line is reached. If there is no change in flow, then in block 408, a check is made to determine if the last instruction in the current line was fetched and executed. If the last instruction was not fetched then control is looped back to block 402.

The subject matter of the proposed amendments to paragraph [0018] are found in paragraph [0015] of the as-filed specification, which is copied below with the supporting passages appearing in bold: